

10/824,270

Express Mail Number EV530656329US PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

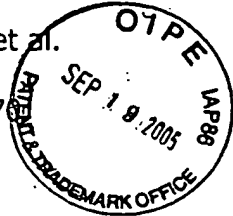
In re Patent Application of:

James Thomas Doyle et al.

Appln. No.: 10/824,270

Filed: April 14, 2004

For: MOS TRANSISTOR WITH SERRATED  
GATE STRUCTURES (as amended)



Group Art Unit: 2818

Examiner: Long K. Tran

AMENDMENT IN RESPONSE TO OFFICE  
ACTION DATED JUNE 29, 2005

INTRODUCTORY COMMENTS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Official Action mailed June 29, 2005, please amend the  
above-identified application as indicated in the following:

**Amendments to the Specification** which begin on page 2 of this paper;

**Amendments to the Claims** which begin on page 3 of this paper;

**Remarks** which begin on page 7 of this paper;

**Appendix A** which begins on page 9 of this paper; and

**Appendix B** which begins on page 10 of this paper

AMENDMENT IN RESPONSE TO  
OFFICE ACTION DATED JUNE 29, 2005

Atty. Docket No. 100-19411  
(P05247-D01-F1)